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APPLICANTS

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** CONTINUING DATA *****
Nme TL

** FOREIGN APPLICATIONS *****
 JAPAN 2002-284817 09/30/2002 *yes TL*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>TL</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 21	INDEPENDENT CLAIMS 8
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Verified and Acknowledged
 Examiner's Signature *TL* Initials

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TITLE
 Semiconductor integrated circuit device and self-test method of memory macro

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